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| L9    | 0         |

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**Database:**  IBM Technical Disclosure Bulletins **Search:**L10 **Search History****DATE:** Wednesday, April 30, 2003 [Printable Copy](#) [Create Case](#)

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| side by side    |   | result set       |                 |
|                 | <i>DB=TDBD; PLUR=YES; OP=OR</i>           |                  |                 |
| <u>L10</u>      | L9  | 0                | <u>L10</u>      |
|                 | <i>DB=DWPI; PLUR= YES; OP=OR</i>          |                  |                 |
| <u>L9</u>       | L8  | 0                | <u>L9</u>       |
|                 | <i>DB=EPAB; PLUR= YES; OP=OR</i>          |                  |                 |
| <u>L8</u>       | L7  | 0                | <u>L8</u>       |
|                 | <i>DB=JPAB; PLUR= YES; OP=OR</i>          |                  |                 |
| <u>L7</u>       | L6  | 0                | <u>L7</u>       |
|                 | <i>DB=PGPB; PLUR= YES; OP=OR</i>          |                  |                 |
| <u>L6</u>       | L5  | 0                | <u>L6</u>       |
|                 | <i>DB=USPT; PLUR= YES; OP=OR</i>          |                  |                 |
| <u>L5</u>       | L3 and "NOR type"                         | 0                | <u>L5</u>       |
| <u>L4</u>       | L3 and NOR                                | 0                | <u>L4</u>       |
| <u>L3</u>       | L2 and (source near6 (graded or gradual)) | 5                | <u>L3</u>       |
| <u>L2</u>       | "non volatile flash memory"               | 369              | <u>L2</u>       |
| <u>L1</u>       | "non volatile flush memory"               | 0                | <u>L1</u>       |

END OF SEARCH HISTORY